

CPP READ HEAD FOR HIGH DENSITY AND SHIELD NOISE SUPPRESSION
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FIELD OF THE INVENTION

The invention relates to the general field of magnetic read heads with particular reference to suppression of noise due to AMR (anisotropic magneto-resistance).

BACKGROUND OF THE INVENTION

The principle governing the operation of most magnetic read heads is the change of resistivity of certain materials in the presence of a magnetic field (magneto-resistance or MR). Magneto-resistance can be significantly increased by means of a structure known as a spin valve where the resistance increase (known as Giant Magneto-Resistance or GMR) derives from the fact that electrons in a magnetized solid are subject to significantly less scattering by the lattice when their own magnetization vectors (due to spin) are parallel (as opposed to anti-parallel) to the direction of magnetization of their environment.

GMR stack 11 is schematically shown in FIG. 1. Its key elements (not shown) are an antiferromagnetic layer whose purpose is to act as a pinning agent for a magnetically

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pinned layer. Lying on the latter is a copper spacer layer on which there is a low coercivity (free) ferromagnetic layer. Magnetic shield layers 12 and 13 lie immediately above and below the spin valve and serve to block out external magnetic influences that might upset the operation of the unit, while leaving it free to interact with magnetic fields above the plane of the figure (i.e. from the storage media).

When the free layer is exposed to an external magnetic field, the direction of its magnetization is free to rotate according to the direction of the external field. The magnetization of the free layer will be in a direction dictated by the minimum energy state. This, in turn, is determined by a number of factors including the crystalline and shape anisotropies.

If the direction of the pinned field is parallel to the free layer, electrons passing between the free and pinned layers suffer less scattering. Thus, the resistance in this state is lower. If, however, the magnetization of the pinned layer is anti-parallel to that of the free layer, electrons moving from one layer into the other will suffer more scattering so the resistance of the structure will increase. The change in resistance of a CIP spin valve is typically 8-20% while for a CPP SV this value can be over 60%.

Most GMR devices have been designed so as to measure the resistance of the free layer for current flowing parallel to its two surfaces. However, as the quest for ever greater

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densities has progressed, devices that measure current flowing perpendicular to the plane (CPP) have begun to emerge. For devices depending on in-plane current, the signal strength is diluted by parallel currents flowing through the other layers of the GMR stack, so these layers should have resistivities as high as possible. In contrast, in a CPP device, the total transverse resistance of all layers, other than the free layer, should be as low as possible so that resistance changes in the free layer can dominate.

Since the two shields, 12 and 13, are made of soft magnetic materials they will sense the bit fields from the disk media by its AMR effect as sense current flow (ΔR_{AMR}) in the shields. ΔR_{AMR} constitutes noise that is super-imposed on the GMR signal. This phenomenon is illustrated in FIG. 2, where an R-H curve of a CPP shield is shown. Note the portion within circle 21. The part of the curve near zero field that has gone negative represents AMR pickup by the shield.

It is the purpose of the present invention to reduce this noise by 5 to 10 times. A method for achieving this is disclosed below.

A routine search of the prior art was performed with the following references of interest being found:

In US 6,515,573, Bong et al. describe a small free layer resulting in reduced noise.

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Smith et al. discuss noise in the free layer in U S 6,473,279. US 6,469,879 (Redon et al) shows that noise occurs if the free layer is too thin. US 5,959,811 (Richardson) discloses noise current in the leads while Rottmayer et al. teach a configuration to reduce noise in the read signal in US 5,784,224.

SUMMARY OF THE INVENTION

It has been an object of at least one embodiment of the present invention to provide a CPP GMR magnetic read head.

Another object of at least one embodiment of the present invention has been that said read head exhibit AMR noise less than about 14 to 20 db.

Still another object of at least one embodiment of the present invention has been to provide a process for manufacturing said read head.

Yet another object of at least one embodiment of the present invention has been to provide a structure that can maintain a very narrow band gap.

These objects have been achieved by coating the upper and lower magnetic shields

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of the read head with highly conductive layers. If the guidelines disclosed in the invention are followed, these objects can be met.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 shows a conventional CPP GMR unit placed between two magnetic shields.

FIG. 2 is an R-H curve of a CPP head with magnetic shield

FIG. 3 is a CPP GMR structure whose shields have been modified in accordance with the present invention.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

As noted earlier, the present invention shows how shield noise may be reduced by a factor of 5 to 10. We achieve this by adding highly conductive layers to both shields and, then, bypassing most of the current that normally flows inside the shield. Said bypass conductors are added at each shield's outside face. This allows the read gap to be made very narrow and can obtain a narrow pulse width (PW_{50}) to achieve higher BPI

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We will disclose the present invention through a description of a process for its manufacture. Said description will also make clear the structure of the present invention.

Referring now to FIG. 3, the process begins with the provision of substrate 31 on which is deposited lower conductive layer 33. This is followed by the deposition (on layer 33) of lower magnetic shield layer 13. A key feature of the invention is that layer 13 must have an electrical resistance {parallel to the plane of the substrate) that is between about 5 and 10 times the in-plane resistance of lower conductive layer 33.

CPP GMR stack 11 is now formed on lower magnetic shield 13. Note that stack 11 is actually surrounded by insulation (not shown) that will serve to support any subsequently deposited layers.

Next, upper magnetic shield layer 12 is deposited on CPP GMR stack 11 followed by the deposition (on upper magnetic shield 12) of upper conductive layer 32. Another key feature of the invention is that layer 32 must have an electrical resistance that is between about 5 and 10 times the in-plane resistance of layer 12.

Typical materials used for the magnetic shields include (but are not limited to) NiFe, CoZrNb, FeAlSi, NiFeCr, NiFe, and Ta. It is important to note that not all these materials would be considered as suitable candidates for magnetic shielding in prior art structures

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because of their relatively high resistivity. Similarly, shields used by the prior art need to be at least 2 microns thick in order to keep their in-plane resistance as low as possible. Shields used in accordance with the present invention may have thicknesses between about 1 and 3 microns. Thus, the in-plane sheet resistance of each magnetic shield may be anywhere in the range of from about 0.15 to about 0.45 ohms per square

Suitable materials for lower conductive layer 33 include (but are not limited to) Cu, Al, Au, and Ag while for upper conductive layer 32 suitable materials include (but are not limited to) Cu, Al, Au, and Ag. Note that it is not required that layers 32 and 33 be of the same material, particularly in situations where it would be advantageous for them to be of different materials.

In general, lower conductive layer 33 will have a resistivity between about 2 and 10 microhm-cm and will be deposited to a thickness between about 0.5 and 5 microns. Similarly (but not necessarily identically) upper conductive layer 32 will have a resistivity between about 2 and 10 microhm-cm and will be deposited to a thickness between about 0.5 and 5 microns.

If the guide-lines disclosed above are followed, the resulting structure will exhibit AMR noise that has been reduced by 14 to 20 dB.

What is claimed is: